





第 1 部分：引言 .....	4
1.1 开始使用之前 .....	4
1.2 包装清单 .....	4
1.3 主板规格 .....	5
1.4 后置面板接口 .....	5
1.5 主板定义平面图（正面） .....	6
第 2 部分:安装 .....	7
2.1 CPU.....	7
2.2 风扇插针.....	7
2.3 系统内存.....	8
2.4 电源供应(主板提供 DC_12V 供电) .....	8
2.5 板载插槽、连接器、插针、跳线 .....	9

1

**1.1**

3

(

**1.2**

F&C\*2)) \*/I

\*

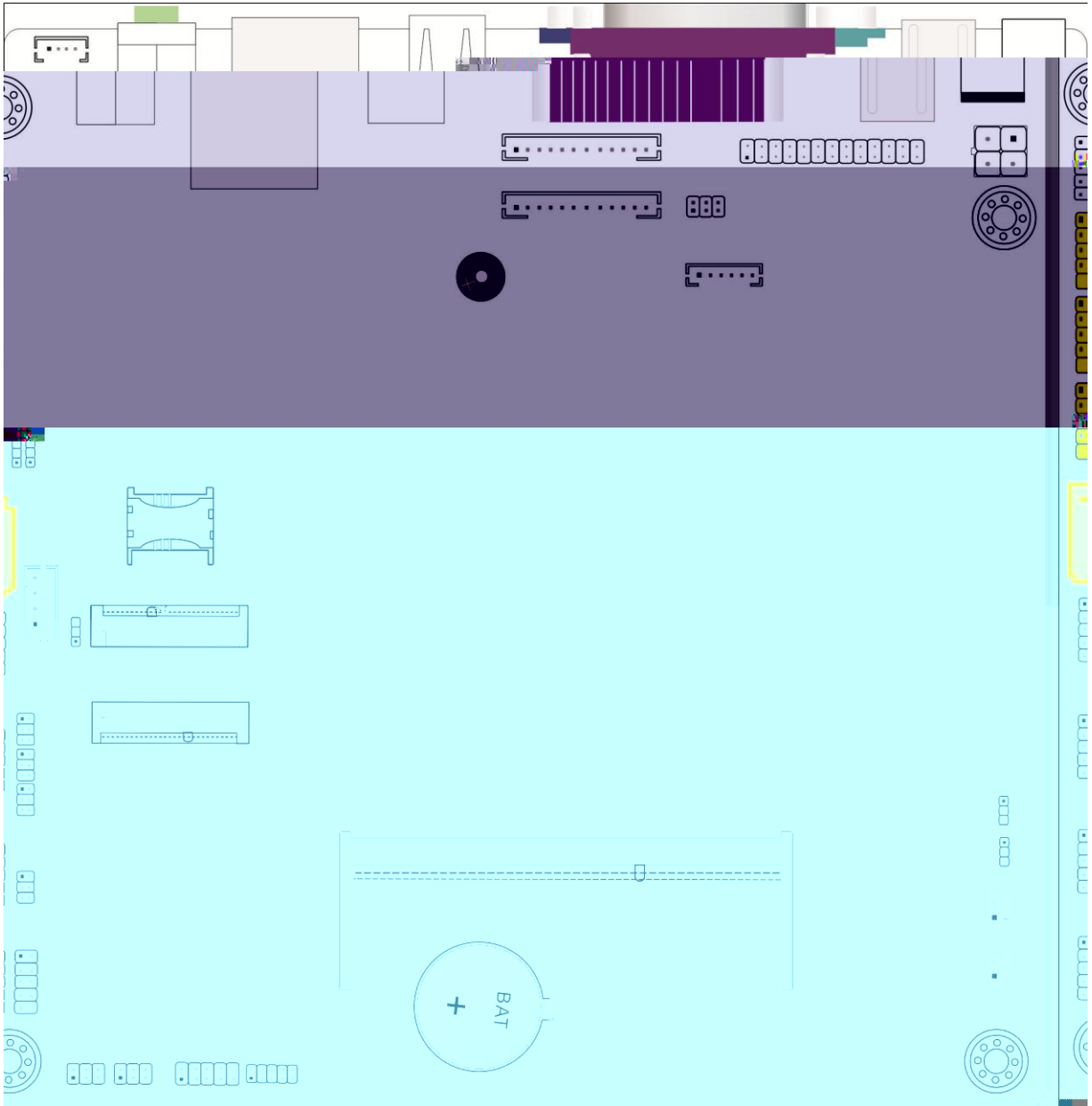
# 1.3

I ( I	B	C*2))	+ ) A	I	
*			, E *	, FA	1
*	:	:	*	:	:
Realtek ALC662		5.1			
*	Realtek RTL8111H	PCI-E			
*	FBGB I B&	! B B"			
E ( I	(	24-bit LVDS	1920×1200		
VGA+LVDS		/			
HF	6×COM RS-232(COM2 3	5V/12V)	COM3	RS-422/485	
	COM4	RS-485			
	9	USB 2.0/1.1(3	IO 6	) 1	USB 3.0
B(H	I (+ HF* I BG	:	E: G&	:	BH&C: D
*	I(E	* C :	* B		
.	HF	.	HF	,	+ ) / + )
*		* 1	I BH		
B(H	*	-	*+	* I	
*		*	:	*	I: G E
*	*	BF			

F

# 1.5

AUDIO-JA305 0.506 RG(U)TJET@MC /Kgen-US)BDC q05.62 714.94 415.32 841.92 rW\*BT/F1 6.48 Tf 0 0 1 131.3 723.34 Tth0196 0.224 0.506 @J896 0.2



**1pin**

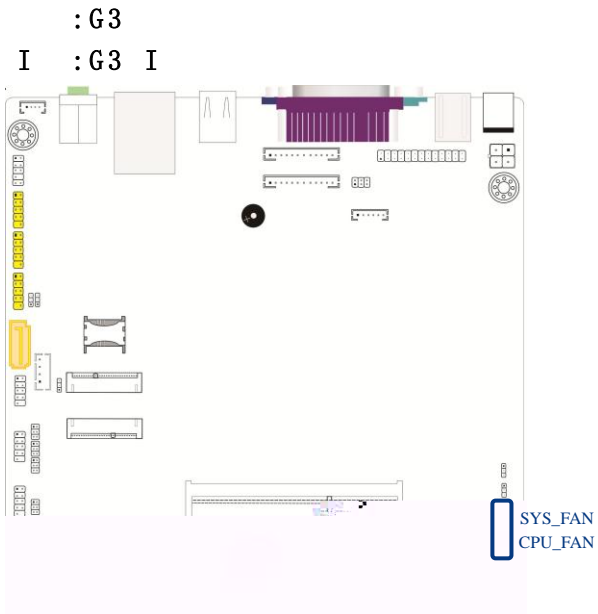
2 :

## 2.1 CPU

B C\*2)) + ) A I

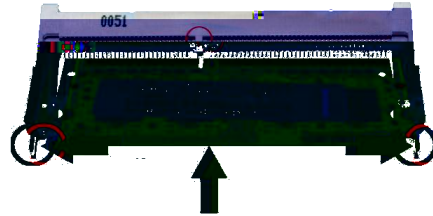
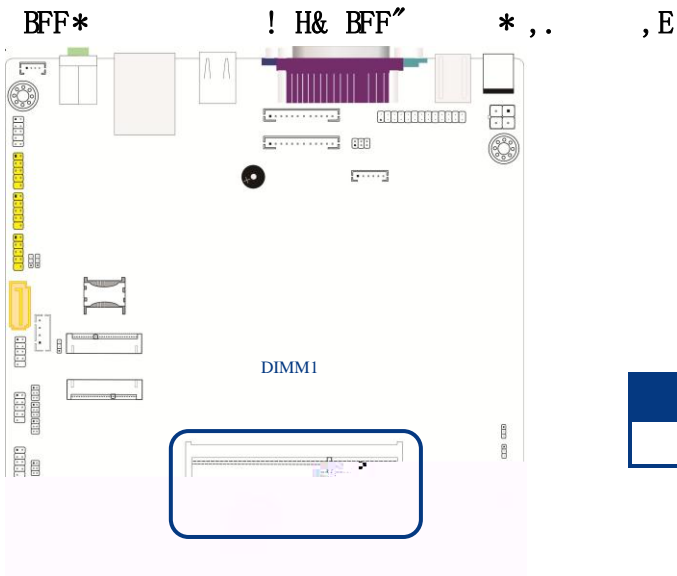
## 2.2

I



I	
*	G
+	*+
,	

## 2.3



I BG				
BFF*	+)–	* ,.	F * , ,	1

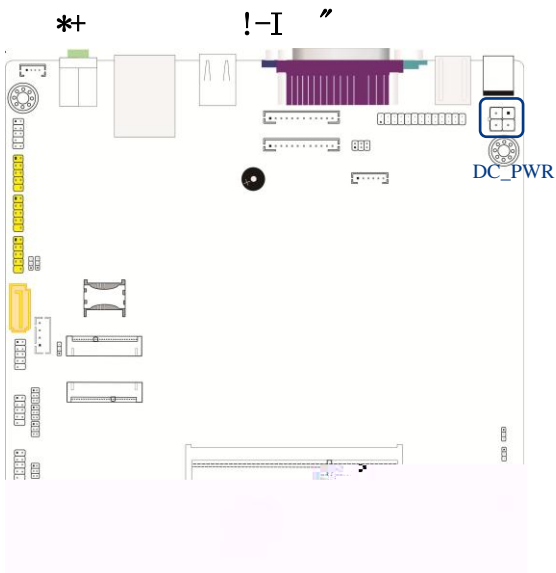
:

\*

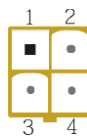
,)

+

## 2.4 ( DC\_12V )



DC\_PWR



I	
*	G
+	G
,	*+
–	*+



**DC\_PWR1**

**IO DC\_jack**

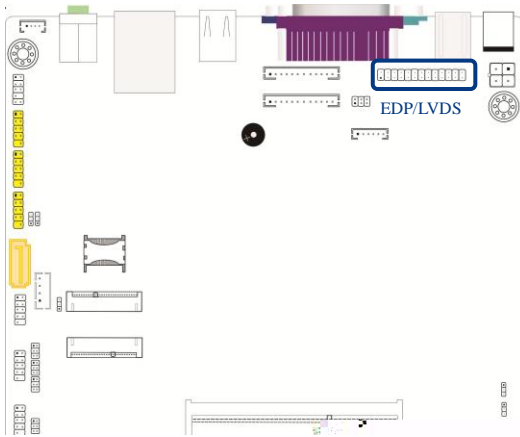
## 2.5

### 2.5.1

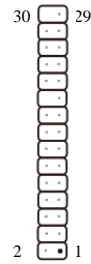
### EDP/LVDS LVDS\_PWR INVERT

### VGA1/2

#### EDP/LVDS



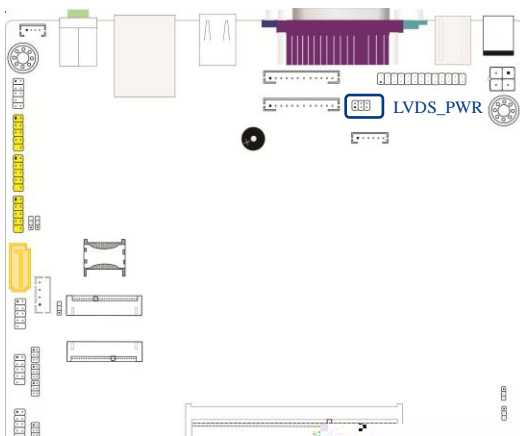
EDP/LVDS



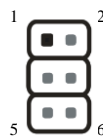
I		I	
*		+	
,		-	G
.	G	/	G
0	E * )&	1	E * )
2	E * *&	*)	E * *
**	E * +&	**+	E * +
*,	G	*-	G
*.	E * ED&	*/	E * ED
*0	E * ,&	*1	E * ,
*2	E + )&	+) )	E + )
++	E + *&	++	E + *
+,	E + +&	+ -	E + +
+.	G	+ /	G
+0	E + ED&	+1	E + ED
+2	E + ,&	, )	E + ,

#### LVDS\_PWR

#### LVDS

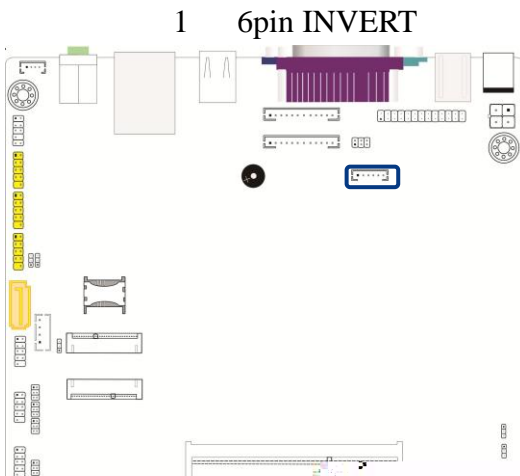


LVDS\_PWR

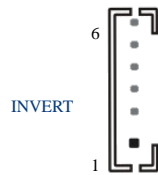


I	
*&+	, ,
, &-	.
. &/	*+

# INVERT



# LVDS



I	
*	*+
+	*+
,	G: E
-	I F
.	G
/	G

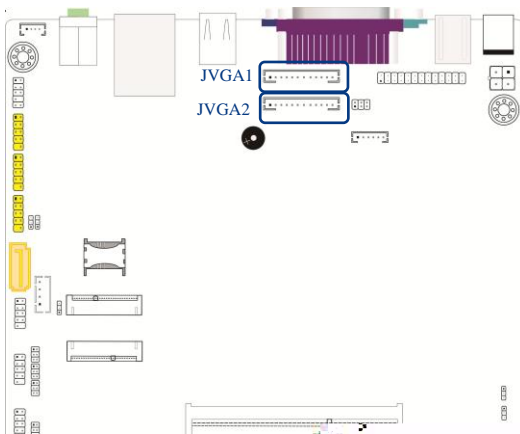
# JVGA1

IO VGA

VGA

# JVGA2

VGA1

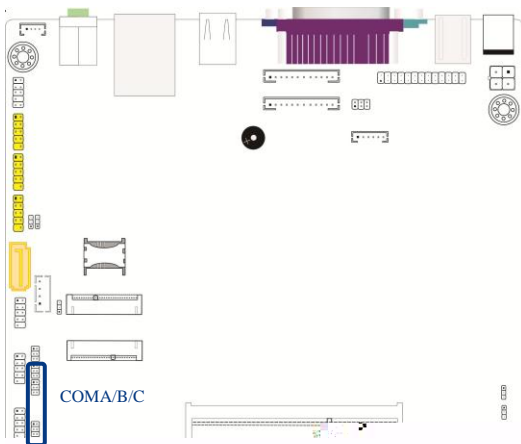


I		I	
*	G	0	
+	G	1	G
,	A G	2	
-	G	*)	G
.		**	: :
/	G	*+	ED



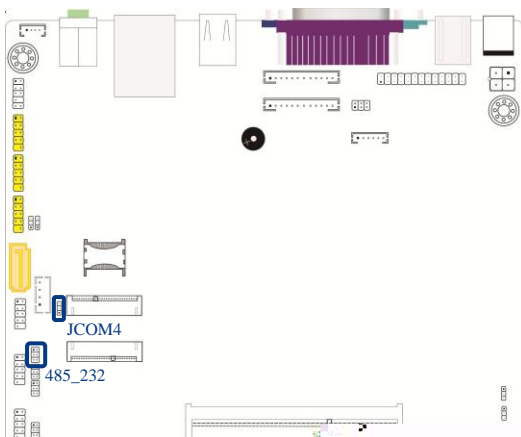


## JCOMA/B/C(



	I				
+, +	C HF: *&+	C HF *&,	C HF +&-	C HF *&,	C HF +&-
-1.	C HF: ,&-	C HF ,&.	C HF -&/	C HF ,&.	C HF -&/
+++	C HF: .&/	C HF ,&.	C HF -&/	C HF ,&.	C HF -&/

## 485\_232(JCOM4)



485\_232

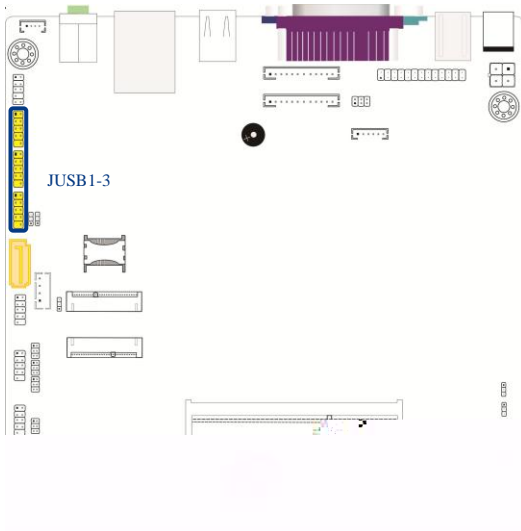
I		
*&, +&-		-1.
,&. -&/		+, +



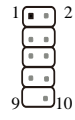
JCOM4

I		
*&+		-1.
+&,		+, +

## JUSB1-3



JUSB1-3

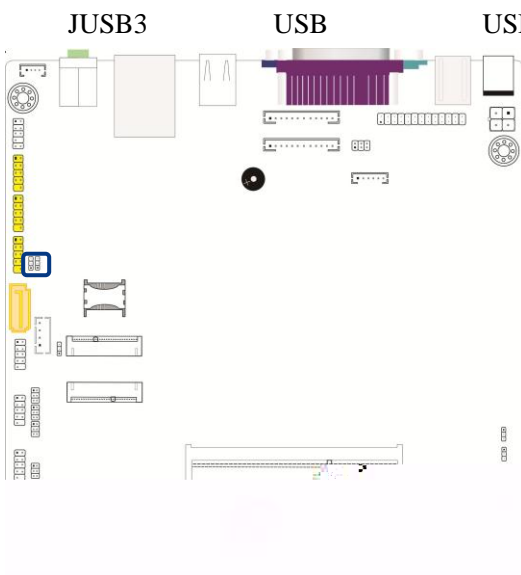


I		I	
*		+	
,		&	
.		/	
0	G	1	G
2	: :	*)	G

**JUSB3(1-3-5-7 Pin)**

**USB**

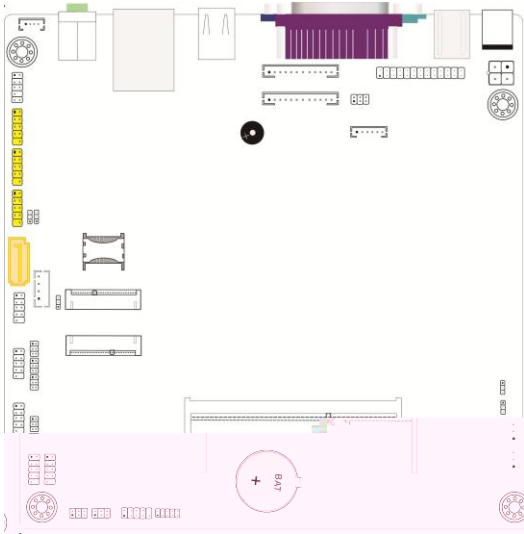
## JSW1/2 USB



	C *	C +
BF :	*&+	*&+
C ,	+& ,	+& ,

## F\_AUDIO

: BH

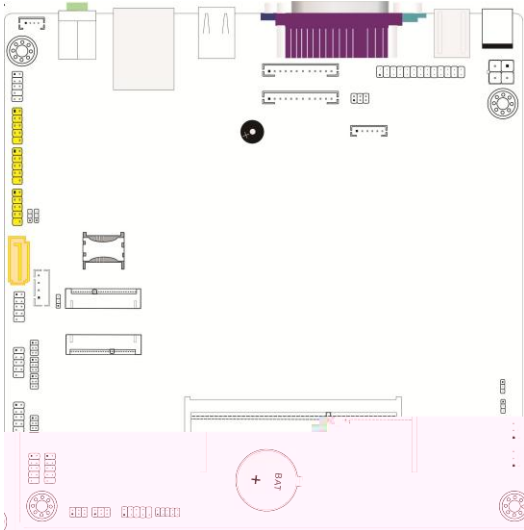


I		I	
*	FB &E	+	: G
,	FB &	-	G
.	EBG H &	/	FB C
0	G	1	
2	EBG H &E	*)	EBG C

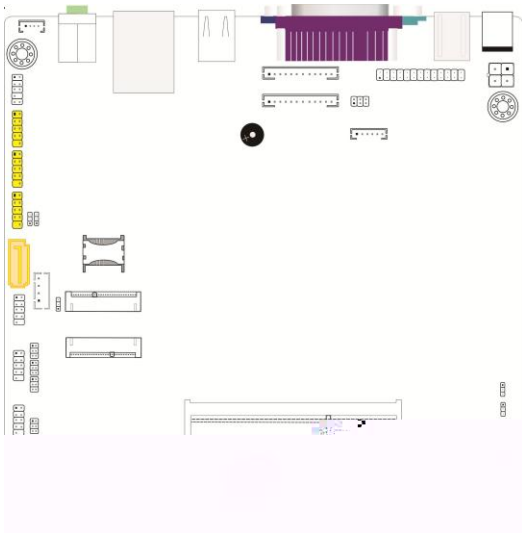
## JGPIO

8bit GPIO

4 4



# JSPK

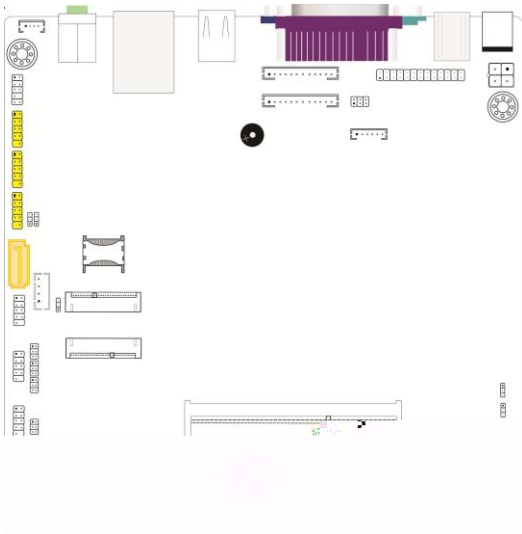


I	
*	: FI &
+	: FI
,	: FI E
-	: FI E&

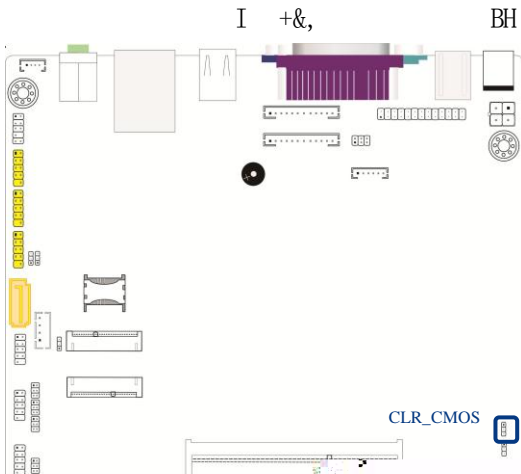
## 2.5.3 F\_PANEL CLR\_CMOS AT\_SEL

### F\_PANEL

\*) I BG



# CLR\_CMOS CMOS



I	
*&+	G
+&,	FH

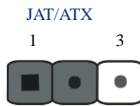
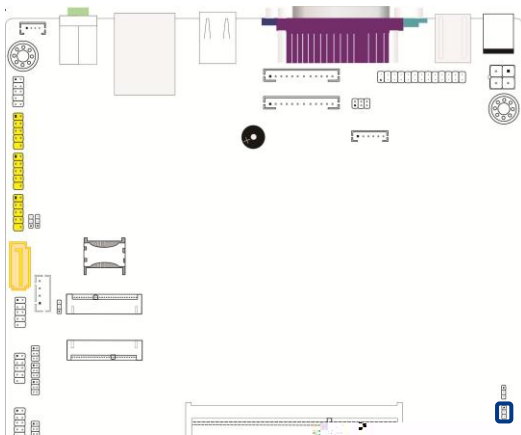
## CMOS :

I +&,

I \*&+

FH

# AT\_SEL



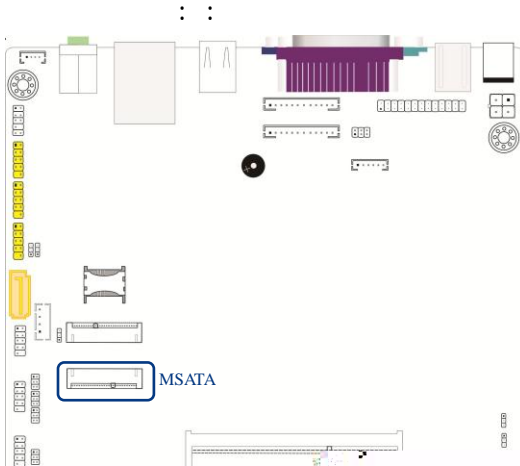
I	
*&+	*+
+&,	:

## 2.5.4

## MSATA SATA SATA\_PWR MINI-PCIE

### SIM

#### mSATA



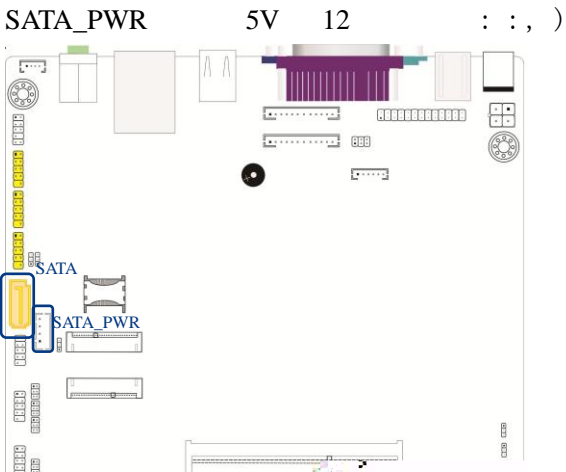
MSATA



**SSD**

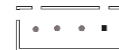
#### SATA 3.0

#### /SATA\_PWR



/ (

SATA\_PWR



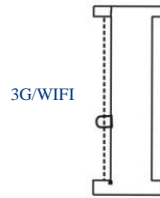
I		I	
*	*+	+	G
,	G	-	.

SATA



**SATA\_PWR**

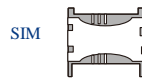
# Mini PCI-E 3G/WIFI



# SIM



3G/4G SIM



#

3

+

